

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

:

: EXAMINER: FRANKLIN, R.

SERIAL NO: 10/735.680

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FILED: DECEMBER 16, 2003

TAKAHITO NAKANO, ET AL.

: GROUP ART UNIT: 2181

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT APPARATUS AND CIRCUIT BOARD AND INFORMATION READOUT

METHOD

AMENDMENT UNDER 37 C.F.R. §1.111

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated April 16, 2007, please amend the aboveidentified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 6 of this paper.